Day: Thursday Date: 1/19/2006

Time: 14:13:22

## Ω.....

## PALM INTRANET

## **Inventor Information for 10/796420**

Inventor Name	City	State/Country
LANDSBERGER, LESLIE M.	WESTMOUNT	CANADA
GRUDIN, OLEG	MONTREAL	CANADA
FROLOV, GENNADIY	MONTREAL	CANADA

Search Another: Application# Search or Patent# Search

PCT / Search or PG PUBS # Search

Attorney Docket # Search

Bar Code # Search

To go back use Back button on your browser toolbar.

Back to PALM | ASSIGNMENT | OASIS | Home page

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	297	circuit and (trimming near resistor) and substrate and heat	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 10:48
L2	29	functional adj resistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 10:49
L3	3996	heat near pulse	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 10:49
L4	0	trimming near substrate near resistance adj value near change	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 10:50
L5	13	trimming near substrate near resistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 10:50
L6	7539	resistance near value near change	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 10:50
L7	1	5 and 6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 10:50
L8	3	1 and 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 10:51
L9	14	1 and 3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 10:51
L10	0	6 and 9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 10:51

L11	29	1 and 6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 10:51
L12	0	3 and 11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 10:51